

Amendments to the Claims

This listing of claims replaces all prior versions and listings of claims in the application.

Listing of Claims

1-10. (Canceled)

11. (Currently Amended) A signal line driver circuit comprising:

a shift register;

a latch circuit, electrically connected to the shift register, comprising:

a plurality of pairs of current source circuits, wherein each of the plurality of pairs of current source circuits is configured to receive a set signal and a signal current, and to control an output current value depending on a value of the signal current; includes a transistor having a gate, a source and a drain; and

a plurality of first switches, wherein each of the plurality of first switch circuits is configured to select one of one pair of current circuits among the plurality of pairs of current circuits; and

a plurality of second switches, wherein each of the plurality of second switch circuits is configured to select the other one of the one pair of current circuits among the plurality of pairs of current circuits; and

a changing over circuit electrically connected to the plurality of pairs of current source circuits and a plurality of signal lines,

wherein each of the plurality of pairs of current source circuits is configured to control an output current value depending on a voltage between the gate and the source of the transistor of the pair of current source circuits that is generated by supplying a signal current to the transistor while the gate and the drain of the transistor are electrically connected to each other;

wherein the changing over circuit is configured to select one pair of current source circuits from the plurality of pairs of current source circuits for electrically connecting to each of the plurality of signal lines, and

wherein the shift register is configured to output the set signal.

12. (Currently Amended) A signal line driver circuit comprising:

a shift register;

a latch circuit, electrically connected to the shift register, comprising:

a plurality of pairs of current source circuits, wherein each of the plurality of pairs of current source circuits is configured to receive a set signal and a signal current, and to control an output current value depending on a value of the signal current; includes a transistor having a gate, a source and a drain;

a first switch provided between the shift register and each of the plurality of pairs of current source circuits; and

a second switch, and

a changing over circuit electrically connected between the plurality of pairs of current source circuits and a plurality of signal lines,

~~wherein each of the plurality of pairs of current source circuits is configured to control an output current value depending on a voltage between the gate and the source of the transistor of the pair of current source circuits that is generated by supplying a signal current to the transistor while the gate and the drain electrically are connected to each other,~~

wherein the changing over circuit is electrically connected to a particular pair of current source circuits through the second switch,

wherein the changing over circuit is configured to select one pair of current source circuits from the plurality of pairs of current source circuits for electrically connecting to each of the plurality of signal lines,

wherein the shift register is configured to output the set signal, and

wherein the first and second switches are configured to be controlled ~~by a~~ based on a same latch pulse.

13-80. (Canceled)

81. (Currently Amended) A signal line driver circuit comprising:

a plurality of current source circuits, ~~wherein each of the plurality of current source circuits includes a transistor having a gate, a source and a drain,~~ wherein each of the plurality of current source circuits is configured to be supplied with a first current and to supply a second current, and wherein a value of the second current depends on a value of the first current; ~~a voltage between the gate and the source of the transistor of the current source circuit that is generated by supplying the first current to the transistor while the gate and the drain are electrically connected to each other;~~

a plurality of first switches, wherein each of the plurality of first switch circuits is configured to select one of one pair of current circuits among the plurality of pairs of current circuits;

a plurality of second switches, wherein each of the plurality of second switch circuits is configured to select the other one of the one pair of current circuits among the plurality of pairs of current circuits;

a plurality of signal lines; and

a selector circuit ~~electrically connected between the plurality of current source circuits and the plurality of signal lines,~~ wherein the selector circuit is configured to select one of the plurality of signal lines to which the second current is supplied.

82-91. (Canceled)

92. (Currently amended) The signal line driver circuit according to claim 11,

wherein each of the plurality of pairs of current source circuits includes a transistor having a gate, a source and a drain and further comprising a capacitor wherein having one electrode of the capacitor is electrically connected to the source of the transistor and the other electrode of the capacitor is electrically connected to the gate of the transistor, and

wherein each of the plurality of pairs of current source circuits is configured to control an output current value depending on a voltage between the gate and the source of the transistor of the pair of current source circuits that is generated by supplying a signal current to the transistor while the gate and the drain of the transistor are electrically connected to each other.

93. (Currently amended) The signal line driver circuit according to claim 12,

wherein each of the plurality of pairs of current source circuits includes a transistor having a gate, a source and a drain and further comprising a capacitor wherein having one electrode of the capacitor is electrically connected to the source of the transistor and the other electrode of the capacitor is electrically connected to the gate of the transistor, and

wherein each of the plurality of pairs of current source circuits is configured to control an output current value depending on a voltage between the gate and the source of the transistor of the pair of current source circuits that is generated by supplying a signal current to the transistor while the gate and the drain of the transistor are electrically connected to each other.

94. (Currently amended) The signal line driver circuit according to claim 81,

wherein each of the plurality of pairs of current source circuits includes a transistor having a gate, a source and a drain and further comprising a capacitor wherein having one electrode of the capacitor is electrically connected to the source of the transistor and the other electrode of the capacitor is electrically connected to the gate of the transistor, and

wherein each of the plurality of pairs of current source circuits is configured to control an output current value depending on a voltage between the gate and the source of the transistor of the pair of current source circuits that is generated by supplying a signal current to the transistor while the gate and the drain of the transistor are electrically connected to each other.

95. (New) A display device comprising:

a latch circuit comprising:

a first unit comprising:

a first current source circuit;

a second current source circuit;

a first switch configured to select one current source circuit into which a signal current is inputted between the first current source circuit and the second current source circuit; and

a second switch configured to select one current source circuit from which a signal current is outputted between the first current source circuit and the second current source circuit; and

a second unit comprising

a third current source circuit;

a fourth current source circuit;

a third switch configured to select one current source circuit into which a signal current is inputted between the third current source circuit and the fourth current source circuit; and

a fourth switch configured to select one current source circuit from which a signal current is outputted between the third current source circuit and the fourth current source circuit; and

a selector circuit configured to select one signal line into which a signal current is supplied from each of the first unit and the second unit.

96. (New) The signal line driver circuit according to claim 95,

wherein each of the first to fourth current source circuits includes a transistor having a gate, a source and a drain and a capacitor having one electrode electrically connected to the source of the transistor and the other electrode electrically connected to the gate of the transistor, and

wherein each of the plurality of pairs of current source circuits is configured to control an output current value depending on a voltage between the gate and the source of the transistor of the pair of current source circuits that is generated by supplying a signal current to the transistor while the gate and the drain of the transistor are electrically connected to each other.